

Attorney's Docket No.:10559/105001/P7639/Intel Corporation

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

The previous drawing changes are resubmitted showing the drawing changes are circled.

Claims 1-17, 19-21 and 23-27 stand rejected under 35 USC 102(b) as allegedly being anticipated by Tanaka. This contention has been obviated by the amendment of the claims herein. Specifically, as explained in the previous official action, Tanaka does show a sequencer block, but does not show that two different inverse discrete cosine transforming functions can be concurrently carried out into same directions. Specifically, as explained in the specification on pages 7 and 8, a hardware savings is obtained by processing both of the DCT's in the same direction in different matrices. The devices need to periodically change order, which they do. However, both of the functions are carried out in the same direction concurrently.

Claims 1 and 8 are amended in this way. This obviates the rejection over Tanaka who does not in any way teach or suggest rearranging the matrices in this way so that the functions are carried out in the same direction on both items.

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Claim 15 has also been amended to better emphasize a second execution of the inverse PCT at the same time as the time that the first intermediate results are being processed. This is further not taught or suggested by the cited prior art.

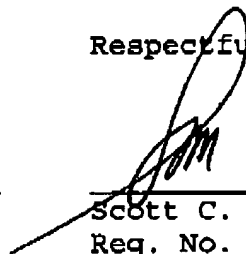
The remaining claims have been amended in a similar way.

Reconsideration and allowance of the above amendments and remarks, therefore, all the claim should be in condition for allowance. A notice on the merits is respectfully requested.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 07/15/03



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Attachment: Drawing Changes